

## EAST Search History

| Ref # | Hits | Search Query   | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|--|---|------------------|---------|------------------|
| S1    | 11   | "6547363"  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2007/10/23 13:47 |
| S65   | 2    | ("20050157002").PN.  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2007/10/23 13:47 |
| S66   | 0    | "347"/\$.ccls. and ((multiple plurality many) near5 circuit) with (determin\$3 detect\$3) with (cartridge) with (rate speed) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/10/23 14:29 |
| S67   | 0    | "347"/\$.ccls. and ((multiple plurality many) with circuit) with (determin\$3 detect\$3) with (cartridge) with (rate speed)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/10/23 14:30 |
| S68   | 2    | "347"/\$.ccls. and (circuit) with (determin\$3 detect\$3) with (cartridge) with (rate speed)                                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/10/23 14:31 |
| S69   | 10   | "347"/\$.ccls. and (circuit) with (cartridge) with (rate speed)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/10/23 14:33 |
| S70   | 34   | "347"/\$.ccls. and (circuit) with (cartridge) with (mode)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/10/23 15:20 |
| S71   | 0    | "347"/\$.ccls. and ((seperate different multiple plurality) with circuit) with (cartridge) with (mode speed rate)            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2007/10/23 15:21 |

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|     |     |   |   |    |    |                  |
|-----|-----|---|---|----|----|------------------|
| S72 | 56  | "347"/\$.ccls. and ((separate different multiple plurality) same circuit) same (cartridge) same (mode speed rate) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:21 |
| S73 | 47  | S72 not S70   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:25 |
| S74 | 66  | "347"/\$.ccls. and (circuit same cradle same controller) and Silverbrook.IN.                                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:27 |
| S75 | 56  | "347"/\$.ccls. and (circuit same cradle same controller) and Silverbrook.IN. and sopec                            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:31 |
| S76 | 597 | "347"/\$.ccls. and (determin\$4 with circuit) same (mode rate speed)  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:31 |
| S77 | 597 | "347"/9,14,19,,4440.ccls. and (determin\$4 with circuit) same (mode rate speed)                                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:35 |
| S78 | 597 | "347"/9,14,19,,44,40.ccls. and (determin\$4 with circuit) same (mode rate speed)                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:35 |
| S79 | 107 | "347"/9,14,19,44,40.ccls. and (determin\$4 with circuit) same (mode rate speed)                                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:46 |

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|     |     |  |   |    |    |                  |
|-----|-----|--|---|----|----|------------------|
| S80 | 259 | "347"/9,14,19,44,40.ccls. and ((cartridge ink tank) with (controller circuit) with (each only))  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:48 |
| S81 | 845 | "347"/\$.ccls. and ((cartridge ink tank) with (controller circuit) with (each only))   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:52 |
| S82 | 16  | "347"/\$.ccls. and ((cartridge) with (controlled operat\$3 regulat\$4 instruct\$4) with (controller circuit) with (each only))                         | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:56 |
| S83 | 60  | "347"/\$.ccls. and ((cartridge) with (controlled operat\$3 regulat\$4 instruct\$4) with (multiple plurality many different) with (controller circuit)) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:10 |
| S84 | 57  | S83 not S82  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 15:57 |
| S85 | 297 | "347"/\$.ccls. and ((cartridge) near (circuit controller))   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:14 |
| S86 | 26  | "347"/\$.ccls. and (determin\$3 with cartridge with use)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:16 |
| S87 | 72  | "347"/\$.ccls. and (determin\$3 with cartridge with operate)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:23 |

## EAST Search History

|     |       |  |   |    |    |                  |
|-----|-------|--|---|----|----|------------------|
| S88 | 29172 | "347"/\$.ccls. and (controller with (separate individual) circuit)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:23 |
| S89 | 378   | "347"/\$.ccls. and (controller with (separate individual plurality multiple) with circuit)                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:23 |
| S90 | 3     | "347"/\$.ccls. and (controller with (separate individual plurality multiple) with circuit with comprise)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:24 |
| S91 | 48    | "347"/\$.ccls. and (controller with (separate individual plurality multiple) with circuit with compris\$3) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:32 |
| S92 | 732   | "347"/\$.ccls. and (module modular) with (circuit controller) and Silverbrook.IN.                          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:34 |
| S93 | 0     | "347"/\$.ccls. and (module modular) with (circuit controller) with on and Silverbrook.IN.                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/10/23 16:34 |